L Number	Hits	Search Text	DB	Time stamp
46	1	("pipelined bus" and soc) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:08
47	21	((pipeline$1 near3 bus) and soc) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:09
45	282	("pipelined bus") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:17
48	1	("pipelined bus" with (flip-flop flipflop "flip flop")) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:18
49	1	("pipelined bus" same (flip-flop flipflop "flip flop")) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:36
54	14	("pipelined bus" same stage) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:29
55	32	("pipelined bus" and (flip-flop flipflop "flip flop")) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:26
60	4	("pipelined bus" same latch$2) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:47
63	47	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same dma same memory) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:26
66	1		USPAT	2004/06/03 09:18
67	6	"6601126"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 09:19
76	1		USPAT	2004/06/03 09:25
77	15	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same "memory bus") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 09:39
80	97	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same dma) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:12
87	1620	("Power PC") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:12

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88	3	("Power PC 405") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:12
91	21480	((flip-flop flipflop "flip flop") same edge) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:26
92	16379	((flip-flop flipflop "flip flop") with edge) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:26
93	1030	((flip-flop flipflop "flip flop") with (edge$1trigger$2)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:27
94	309	(((flip-flop flipflop "flip flop") with (edge$1trigger$2)) same latch) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:27
95	214	(((flip-flop flipflop "flip flop") with (edge$1trigger$2)) with latch) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:28
96	8	(((flip-flop flipflop "flip flop") with (edge$1trigger$2)) with latch with known) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:31
97	179	(((flip-flop flipflop "flip flop") with instead) with latch) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:33
98	1738	(((flip-flop flipflop "flip flop") with (benefit advantage))) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:33
99	1683	(((flip-flop flipflop "flip flop") with (advantage))) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:33
100	95	(((flip-flop flipflop "flip flop") with (advantage) with latch)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 13:35
101	1	((registered with interconnect) same (point$1to$1point)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 13:36
102	0	(("registered with interconnect")) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 13:36
103	6	(("registered interconnect")) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 13:37

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104	137	("bus interface" with (flip$1flop flipflop)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 13:53
105	22	((flip$1flop flipflop) adj interface) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:04
106	238	(point$1to$1point adj bus) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:18
107	1143	(handshake with bus) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:18
108	5	(handshake with bus with advantage) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:19
109	293	(handshake.ti.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:20
110	58	(handshake.ti. and bus) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:30
116	238	(712/233.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:33
111	896	(710/305.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:36
112	187	(710/106.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:40
113	963	(710/100.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:43
114	53	(710/118.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:46
115	31	(710/125.ccls.) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:53
-	141107	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 15:46

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–	33	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same core same bus same peripheral) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:52
–	7	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same core same bus same peripheral) and pipeline and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 16:00
–	49	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) and (bus with pipeline)) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 08:04

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